

MAR BASELIOS COLLEGE OF ENGINEERING AND TECHNOLOGY (AUTONOMOUS)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Academic Year: 2026 - 2027 ODD SEMESTER

Class Time Table

Semester: S5 EC2			Room No: B 112			Start date:01/07/2026			End date: 05/11/2026		
Time ►	08.20 - 09.10	09:15 - 10:15	10:15 - 10:35	10:35 - 11:35	11:35 - 12:35	12:35 - 1:30	01:30 - 02:25	02:25 - 03.20	03:20 - 03:35	03:35 - 04:30	04:40 - 05:30
Day ▼	0	1	B R E A K	2	3	L B U R N E C A H K	4	5	B R E A K	6	7
Monday		EMT		DSP	ADC		MFE	EMT		PE - I	HON
Tuesday	MIN	ADC		EMT	PE - I		DSP	ADC		MFE	HON
Wednesday	MIN	DSP (T)		PE - I	EMT (T)		CE LAB (A)/ DSP LAB (B)			CE LAB (A)/ DSP LAB (B)	
Thursday	HON	PE - I		DSP	MFE		DSP	EMT		ADC	MIN
Time ►	08.20 - 09.10	09:15 - 10:15		10:35 - 11:35	11:35 - 12:35	12:35 - 02:00	02:00 - 02:50	02:50 - 03:40		03:40 - 04:30	04:40 - 05:30
Friday		ADC (T)		SDP			CE LAB (B)/ DSP LAB (A)				

Slot	Category	Course Code	Course Name	TT code	Name of Faculty	
A	PCC	23ECL30A	Analog and Digital Communication	ADC	Dr. Lani Rachel Mathew	
B	PCC	23ECL30B	Digital Signal Processing	DSP	Ms. Deepa P L	
C	PCC	23ECL30C	Electromagnetic Field Theory	EMT	Ms. Nithya Mohanan	
D	PEC	Program Elective I		PE I		
		23ECL31G	Biomedical Engineering		Dr Jayakumari J	B 116 (DCF)
		23ECL31B	Power Electronics		Mr Jinu Baby	B 109
		23ECL31E	Computer Architecture		Ms Ann Mary Alex	B 112
E	HSC	23HSL00A	Management for Engineers	MGE	Mr. H. Aravind Sarma / Mr. X1	
S	PCC	23ECP30A	Communication Lab	CE LAB	Dr. Lani Rachel Mathew/ Mr. Jinu Baby/ Mr. Sherry Varghese George	
T	PCC	23ECP30B	Digital Signal Processing Lab	DSP LAB	Ms. Deepa P L/ Ms. Amritha B J/ Ms Nithya Mohanan/ Mr. X1	
			SDP Simulink and PCB design	SDP	Ms Nithya Mohanan / Dr. Ancy S Anselam	

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Slot	Category	Course Code	Course Name	TT code	Name of Faculty	
			Minor			Room No
M/H	VAC- Minor	23EEL3MA	EEE Basket I, (Embedded systems for Industrial applications)- Raspberry Pi - Python Interface for Electrical Engineering	MIN	Mr./Ms. X	B 05
M/H	VAC- Honours	23EEL3HA	ECE Basket I: (VLSI and Embedded Systems) - FPGA based System Design	HON	Ms. Amritha B J	B 112

Staff Advisor Details	Room No.	Contact Email	Contact Phone number
Dr. Lani Rachel Mathew	B 110	lani.mathew@mbcet.ac.in	9995877394
Mr Alex V	B 111	alex.v@mbcet.ac.in	9747653451
Mr. H. Aravind Sarma	B 111	aravindsarma.h@mbcet.ac.in	7356683623

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Modified on:

Prepared by: Ms. Deepa P L
Dr. Vineetha Mathai

Dr. Luxy Mathews
Dr. Luxy Mathews
HoD
Dept. of ECE

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