

UPCOMING EVENT

The VLSI & Embedded System Research Group, Dept. of ECE, is organizing a Five-Day **offline FDP** on “**VLSI Design and Verification: A Hands-on Approach (VDV’25)**” from **10–14 November 2025** at MBCET. The programme offers a blend of theoretical sessions and practical training using Cadence and MathWorks tools. Faculty and Research Scholars from the ECE and related disciplines are eligible to apply.

💰 **Registration Fee:** ₹1500 | 🏢 **Deadline:** 03 Nov 2025 | 🔗 **Register here:** [<https://tinyurl.com/FDP-VDV2025>]

ABOUT THE PROGRAMME

Objectives of the Programme

The Faculty Development Program on “VLSI Design and Verification: A Hands-on Approach” aims to equip participants with both theoretical insights and practical skills in modern VLSI methodologies. Through sessions on the VLSI lifecycle, hands-on training with Cadence and MATLAB/MathWorks tools, and discussions on emerging trends like AI-enabled VLSI and RISC-V, the program bridges academic knowledge with industry practices, enabling participants to stay updated with the latest advancements in design and verification.

Eligibility Criterion

The FDP is open to faculty members and research scholars of Electronics and Communication Engineering, and related disciplines.

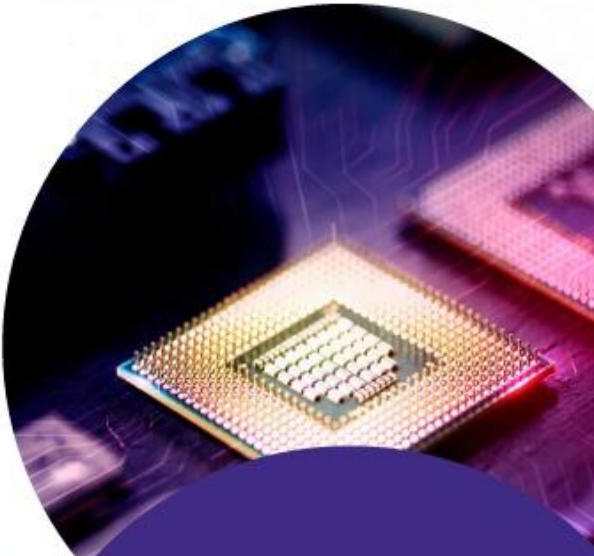
REGISTRATION DETAILS

Registration URL:
<https://tinyurl.com/FDP-VDV2025>

Last date of online registration: 3/11/2025

Registration Fee: Rs. 1500/-

Account details:
Account name: ECE WORKSHOPS
Account No: 0483053000018236
IFSC code: SIBL0000483
Branch: South Indian Bank,
Nalanchira,
Thiruvananthapuram



FDP ON
**VLSI DESIGN AND VERIFICATION:
A HANDS-ON
APPROACH**
10 - 14 NOVEMBER 2025

Organized by
**VLSI AND EMBEDDED SYSTEM
RESEARCH GROUP, DEPARTMENT OF
ELECTRONICS AND COMMUNICATION
ENGINEERING, MBCET**

