

MAR BASELIOS COLLEGE OF ENGINEERING AND TECHNOLOGY (AUTONOMOUS)
DEPARTMENT : ELECTRONICS AND COMMUNICATION ENGINEERING
Academic Year: 2024-2025 ODD SEMESTER

Class Time Table

Semester: S5 EC2		Room No: B112		Start date: 17/07/2024		End date: 21/11/2024						
Time ▶	08:20-09:10	09:15-10:15	10:15-10:35	10:35-11:35	11:35-12:35	12:35-13:30	13:30-14:25	14:25-15:20	15:20-15:35	15:35-16:30	16:40-17:30	
Day ▼	1*	2	B R E A K			L U N C H B R E A K	5	6	B R E A K			8*
Monday		DSP	CS	DSP	DSP		ADC	CS(T)	IEFT			
Tuesday	MINOR	LIC	IEFT	DSP	DSP		CS	ADC	LIC(T)			
Wednesday	MINOR	CS	IEFT	ADCT)	ADCT)		LIC	SDP	SDP			
Thursday	MINOR	ADC	DM	DSP(T)	DSP(T)		DSP LAB (A) / AIC LAB (B)	DSP LAB (A) / AIC LAB (B)	DSP LAB (A) / AIC LAB (B)			
Time ▶	08:20-09:10	09:15-10:15	10:35-11:35	11:35-12:35	12:35-14:00	14:00-14:50	14:50-15:40	15:40-16:30	16:40-17:30			
Friday	MINOR	ADC	LIC	DM	LUNCH BREAK		DSP LAB (B) / AIC LAB (A)	DSP LAB (A)				

*Periods 1 and 8 will be used for Honours/Minors/ Remedial/Extra Classes

Slot	Category	Course Code	Course Name	TT code	Faculty Name	Remarks
A	PCC	EC1U30A	Linear Integrated Circuits	LIC	Ms.JEENA VIJI	
B	PCC	EC1U30B	Digital Signal Processing	DSP	Ms. DEEPA P. L	
C	PCC	EC1U30C	Analog and Digital Communication	ADC	Dr. JAYAKUMARI J	
D	PCC	EC1U30D	Control Systems	CS	Dr. M. J. JAYASHREE	
E	HSC	HS0U30A	Industrial Economics & Foreign Trade	IEFT	Mr. VINU	
F	MNC	NC0U30A	Disaster Management	DM	Ms.MERIN PHILIP	
S	PCC	EC1U38A	Analog Integrated Circuits and Simulation Lab	AIC LAB	Ms.JEENA VIJI/Mr. JINU BABY	
T	PCC	EC1U38B	Digital Signal Processing Lab	DSP LAB	Ms. GOURI NANDHANA /Ms. DEEPA .P. L.	
M	VAC	MA0M30A	Randaom Process and Queuing Theory	MINOR	Ms.ANASWARA JOHNY & Ms.AKHILA MOHAN	B305B
	SDP		SDP - Simulink and PCB Design	SDP	Dr. ANCY S. ANSELAM /Dr. LANI RACHEL MATHEW	

Staff Advisor Details			
Room No.	Contact Email	Contact Phone number	
B 100	ancy.anselam@mbcet.ac.in	9446534657	Dr. Ancy.S. Anselam
B 111	anu.philip@mbcet.ac.in	8547716480	Ms. Anu Susan Philip
B 311	gouri.nandhana@mbcet.ac.in	9496691596	Ms. Gouri Nandhana

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Prepared by: Mr. Niyas K Hanefra, Ms.Salga Ann Jacob

HOD: Dr. Laxy Mathews

