

Mar Baselios College of Engineering & Technology
Department: Electronics & Communication
Class Time Table

Academic Year: **2023-2024** Semester: **S5EC2** (Room No: **B-109**) Start date: 04/09/2023, Enddate:16/01/2024

Day▼ Time▶	9.15 – 10.15 (1)	10.15 – 11.15 (2)	BREAK	11.35- 12.35 (3)	BREAK	1.35 – 2.30 (4)	2.30 – 3.25 (5)	3.35-4.30 (6)
MON	ADC	DM		IEFT		SDP	DM	
TUE	CS	ADC		LIC(T)		DSP	CS	LIC
WED	CS	LIC		DSP		ADC	IEFT	ADC(T)
THU	IEFT	ADC		DSP		AIC LAB (A)/ DSP LAB (B)		
FRI	CS(T)	DSP(T)		LIC		AIC LAB (B)/ DSP LAB (A)		

Slot	Sub Code	Category	Subject Name	Syllabus code	Faculty Name	Remarks
A	EC1U30A	Theory	Linear Integrated Circuits	LIC	Mr. JINU BABY	
B	EC1U30B	Theory	Digital Signal Processing	DSP	Ms. DEEPA P. L.	
C	EC1U30C	Theory	Analog and Digital Communication	ADC	Dr. SWAPNA P. S.	
D	EC1U30D	Theory	Control Systems	CS	Dr. M. J. JAYASHREE	
E	HS0U30A	Theory	Industrial Economics & Foreign Trade	IEFT	Mr. VINU V	
F	NC0U30A	Theory	Disaster Management	DM	SHERRY VARGHESE GEORGE	
S	EC1U38A	Lab	Analog Integrated Circuits and Simulation Lab	AIC LAB	Mr. JINU BABY/Mr.SHERRY VARGHESE GEORGE	
T	EC1U38B	Lab	Digital Signal Processing Lab	DSP LAB	Ms. DEEPA P. L./Mr.SREEKUMAR M G	
		SDP	SDP in Simulink and PCB Design	SDP	Dr. LUXY MATHEWS/Dr. ANCY S. ANSELAM	

Advisors List

Sl.No.	Title	Name
1	Co-ordinator	Dr.SWAPNA.P.S
2	Advisor	Ms.LAKSHMY.S/ Ms.NIKHILA. P
3	Advisor	Mr.JINU BABY