

**Mar Baselios College of Engineering & Technology**  
**Department: Electronics & Communication**  
**Class Time Table**

Academic Year: **2023-2024** Semester: **S5EC1** (Room No: **B-112**) Start date: 04/09/2023, Enddate:16/01/2024

Day▼ Time▶	9.15 – 10.15 (1)	10.15 – 11.15 (2)	BREAK	11.35- 12.35 (3)	BREAK	1.35 – 2.30 (4)	2.30 – 3.25 (5)	3.35-4.30 (6)
MON	IEFT	ADC		DM		LIC	DSP(T)	CS
TUE	CS	DSP		ADC		AIC LAB (A)/ DSP LAB (B)		
WED	CS	DSP		CS(T)		AIC LAB (B)/ DSP LAB (A)		
THU	LIC	ADC		IEFT		DM	DSP	LIC
FRI	LIC(T)	SDP		SDP		ADC	IEFT	ADC(T)

Slot	Sub Code	Category	Subject Name	Syllabus code	Faculty Name	Remarks
A	EC1U30A	Theory	Linear Integrated Circuits	LIC	Mr. ANOOP K. JOHNSON	
B	EC1U30B	Theory	Digital Signal Processing	DSP	Dr. LANI RACHEL MATHEW	
C	EC1U30C	Theory	Analog and Digital Communication	ADC	Ms. SALGA ANN JACOB	
D	EC1U30D	Theory	Control Systems	CS	Mr. SHIRAS S N	
E	HS0U30A	Theory	Industrial Economics & Foreign Trade	IEFT	Mr. VINU V	
F	NC0U30A	Theory	Disaster Management	DM	Ms. ANN MARY ALEX	
S	EC1U38A	Lab	Analog Integrated Circuits and Simulation Lab	AIC LAB	Mr. ANOOP K. JOHNSON/Mr.ARUN J S	
T	EC1U38B	Lab	Digital Signal Processing Lab	DSP LAB	Dr. LANI RACHEL MATHEW/Ms.GOURI NANDHANA	
		SDP	SDP in Simulink and PCB Design	SDP	Dr. ANCY S. ANSELAM/Dr.LUXY MATHEWS	

Advisors List

<b>Sl.No.</b>	<b>Title</b>	<b>Name</b>
1	Co-ordinator	Mr.SHIRAS. S .N
2	Advisor	Ms.SALGA ANN JACOB
3	Advisor	Ms.MERIN PHILIP