

**Department of Electronics & Communication**  
**Mar Baselios College of Engineering & Technology**  
**Time Table of Fifth Semester B.Tech (EC - 2) (October 2022 to 2023)**

Start date: 17/10/2022

	9.15 – 10.15 (1)	10.15 – 11.15 (2)	<b>BREAK</b>	11.35-12.35 (3)	<b>BREAK</b>	1.35 – 2.30 (4)	2.30 – 3.25 (5)	3.35-4.30 (6)
<b>MONDAY</b>	<b>CS</b>	<b>DSP</b>		<b>ADC</b>		<b>AIC LAB (A)/ DSP LAB (B)</b>		
<b>TUESDAY</b>	<b>LIC</b>	<b>IEFT</b>		<b>CS</b>		<b>AIC LAB (B)/ DSP LAB (A)</b>		
<b>WEDNESDAY</b>	<b>SDP</b>	<b>SDP</b>		<b>DM</b>		<b>IEFT</b>	<b>ADC</b>	<b>CS (T)</b>
<b>THURSDAY</b>	<b>DSP</b>	<b>CS</b>		<b>ADC</b>		<b>LIC</b>	<b>CS</b>	<b>ADC (T)</b>
<b>*FRIDAY</b>	<b>DSP</b>	<b>LIC</b>		<b>DSP (T)</b>		<b>LIC (T)</b>	<b>DM</b>	<b>IEFT</b>

Subject Code	Subject	Faculty
EC1U30A	Linear Integrated Circuits	Mr. JINU BABY
EC1U30B	Digital Signal Processing	Ms.VINEETHA MATHAI
EC1U30C	Analog and Digital Communication	Ms. TEENA RAJAN
EC1U30D	Control Systems	Dr. ANCY S. ANSELAM
HS0U30A	Industrial Economics & Foreign Trade	Mr. VINU
NC0U30A	Disaster Management	Ms.SAUMYA THOMAS
	SDP	Dr. LANI RACHEL MATHEW/Dr. ANCY S. ANSELAM
EC1U38A	Analog Integrated Circuits and Simulation Lab	Mr. JINU BABY/Ms.TEENA RAJAN
EC1U38B	Digital Signal Processing Lab	Ms.VINEETHA MATHAI/Ms.DEEPA PL

<b>ADVISORS</b>	<b>Ms Teena Rajan( C )</b>
	Dr Sreedevi P.
	Dr. Lani Rachel Mathew

\*FRIDAY

Period 4      2.00pm - 2.50pm  
 Period 5      2.50pm -3.40pm  
 Period 6      3.40pm-4.30pm

**Dr. JAYAKUMARI J.**  
**Head of Department**  
**Dept. of EC**