

**Department of Electronics & Communication
Mar Baselios College of Engineering & Technology**

Time Table of Fifth Semester B.Tech (EC - 1) ((November 2021 to March 2022))

Start date: 18/11/2021

	9.15 – 10.15 (1)	10.15 – 11.15 (2)	BREAK	11.35-12.35 (3)	BREAK	1.40 – 2.35 (4)	2.40 – 3.30 (5)	3.40-4.30 (6)
MONDAY	DM	A&D		CS		DSP	A&D	DSP(T)
TUESDAY	CS	DSP		IEF		AIC LAB(A)/DSP LAB(B)		
WEDNESDAY	DSP	LIC		IEF		A&D(T)	CS	CS(T)
THURSDAY	A&D	LIC		DSP		LIC	IEF	DM
*FRIDAY	CS	A&D		LIC(T)		AIC LAB(B)/DSP LAB(A)		

Subject Code	Subject	Faculty
ECT 301	Linear Integrated Circuits(LIC)	Ms. ANCY JOY (AYJ)
ECT 303	Digital Signal Processing(DSP)	Ms. DEEPA P. L. (DPL)
ECT 305	Analog and Digital Communication(A&D)	Ms. TEENA RAJAN (TRN)
ECT 307	Control Systems(CS)	Mr. NIYAS K HANEEFA(NKH)
HUT 300	Industrial Economics and Foreign Trade(IEF)	Mr. VINU V
MCN 301	Disaster Management(DM)	Ms. LAKSHMY S(LKS)
ECL 331	Analog Integrated Circuits and Simulation Lab(AIC LAB)	Ms. ANCY JOY (AYJ)
ECL 333	Digital Signal Processing Lab(DSP LAB)	Ms. DEEPA P. L. (DPL)

*FRIDAY

Period 1	9.15am-10.15am
Period 2	10.15am-11.15am
Period 3	11.35am - 12.35am
Period 4	2.00pm - 2.50pm
Period 5	2.50pm -3.40pm
Period 6	3.40pm-4.30pm

Sd/-

Dr. JAYAKUMARI J.
Head of Department
Dept. of EC