

**Department of Electronics & Communication**  
**Mar Baselios College of Engineering & Technology**

**Time Table of Fifth Semester B.Tech (EC - 2) (November 2021 to March 2022)**

Start date:18/11/2021

Modified on:10/01/2022

	9.15 – 10.15 (1)	10.15 – 11.15 (2)	BREAK	11.35-12.35 (3)	BREAK	1.40 – 2.35 (4)	2.40 – 3.35 (5)	3.40-4.30 (6)
MONDAY	A&D	DSP		CS		AIC LAB(A)/DSP LAB(B)		
TUESDAY	A&D	DM		LIC(T)		CS	DSP	
WEDNESDAY	LIC	CS		A&D(T)		DSP(T)	A&D	IEF
THURSDAY	A&D	LIC		IEF		AIC LAB(B)/DSP LAB(A)		
*FRIDAY	DSP	LIC		DM		CS	IEF	CS(T)

Subject Code	Subject	Faculty
ECT 301	Linear Integrated Circuits(LIC)	Ms. SALGA ANN JACOB(SAJ)
ECT 303	Digital Signal Processing(DSP)	Ms. LIYA JOHNSON( LNJ)
ECT 305	Analog and Digital Communication(A&D)	Dr. SREEDEVI P. (SDP)
ECT 307	Control Systems(CS)	Ms. SAUMYA THOMAS(STS)
HUT 300	Industrial Economics and Foreign Trade(IEF)	Mr. VINU V
MCN 301	Disaster Management(DM)	Ms. AMRITHA B. J. (ABJ)
ECL 331	Analog Integrated Circuits and Simulation Lab(AIC LAB)	Mr. JINU BABY (JBY)
ECL 333	Digital Signal Processing Lab(DSP LAB)	Ms. LIYA JOHNSON( LNJ)

\*FRIDAY

Period 1	9.15am-10.15am
Period 2	10.15am-11.15am
Period 3	11.35am - 12.35am
Period 4	2.00pm - 2.50pm
Period 5	2.50pm -3.40pm
Period 6	3.40pm-4.30pm

Sd/-

**Dr. JAYAKUMARI J.**  
**Head of Department**  
**Dept. of EC**

