Registration details:

For registration, use the following link : tiny.cc/SIDC21

No Registration Fee

Google MEET link will be provided to all the registered participants through email.
E-certificates will be provided to the participants upon the successful completion of the workshop and submission of Microproject.

Organizing Committee

Rev. Fr. John Vilayil
Bursar, MBCET

Dr. Abraham T. Mathew
Principal, MBCET

Prof. S. Viswanatha Rao
Vice Principal, MBCET

Dr. M. J. Jayashree
HoD, Dept. of ECE, MBCET

Co-ordinator

Ms. Ann Mary Alex
Assistant Professor, Dept. of ECE
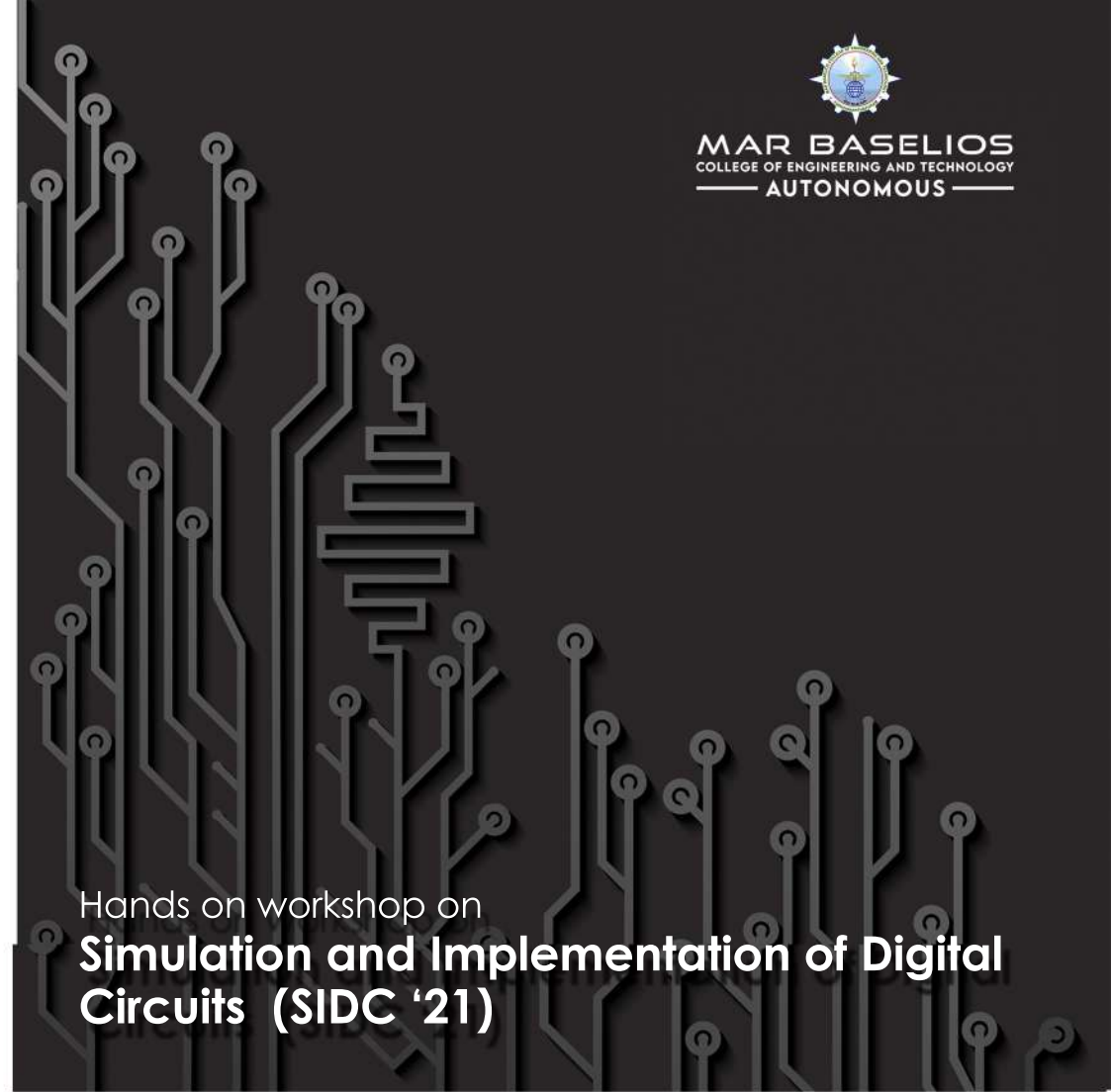MBCET
Ph : **-** 9446187216

MAR BASELIOS
COLLEGE OF ENGINEERING AND TECHNOLOGY
— AUTONOMOUS —

Hands on workshop on
Simulation and Implementation of Digital Circuits (SIDC '21)

15-19- March, 2021

Organized by
Department of Electronics & Communication Engineering

The Institution

Mar Baselios College of Engineering and Technology (MBCET) was established in the year 2002 by the Major Archdiocese of Trivandrum with the noble objective of providing quality Technical Education based on fundamental human values, without any distinction of caste, creed or religion. Being a proud part of the Mar Ivanios Vidyanagar on the blessed Bethany Hills, deriving the inner strength of truth and goodness from the visionary patrons, MBCET inspires the aspirations of generations of knowledge-seekers. The college aims at achieving the integral development of human personality and is just 8kms away from Trivandrum Central Railway Station and 12 Kms from the Trivandrum International Airport. MBCET offers B Tech programmes in five branches with an intake of 120 each, M Tech programmes in five specializations and Ph D programmes. All the B.Tech programmes are accredited by NBA and the Institution is accredited by NAAC with 'A' Grade (CGPA:3.l3). The UGC has conferred Autonomous status on MBCET which has been notified by the KTU as per the direction of the Govt. of Kerala

The Department

The Department of Electronics & Communication Engineering focuses on imparting a solid foundation, both theoretical and hands-on work experience, across the vast spectrum of technologies in Electronics as well as Communication Engineering. The department has a team of well qualified and competent faculty who are active in research and research guidance with a number of publications to their credit. To encourage research and development, the department regularly organizes Conferences, Seminars, Short Term Training Programs etc. The department has also taken up sponsored research projects from R&D institutions and the faculty members are actively involved in research activities.

The Event

Logic system design plays an important role in understanding the impact of electrical and computer engineering solutions in a global and societal contexts. This hands on workshop gives an insight into the various simulation and implementation tools used for the design of digital circuits.

Programme Contents

Day 1	Basics of Digital Circuits and Introduction to Verilog Programming
Day 2	Verilog Programming - Combinational and Sequential Circuits, Simulation using EDA playground and Xilinx ISE
Day 3	Familiarization of FPGA board. Introduction to TinkerCAD
Day 4	Familiarisation of Digital IC Trainer kit
Day 5	Implementation of digital circuits in trainer kit. Micro project

Resource Persons

Ms. Ancy Joy
Ms. Amritha B. J
Assistant Professors, Dept. of ECE
MBCET

Eligibility

SIDC'21 is a staff training programme in which Faculty and Technical staff from AICTE approved Engineering colleges are eligible to apply .